Signals & Systems

Fall Semester 2011

COURSE ORGANIZATION

Homework 15%
2 Exams 25% each
Final 35%

Lecture A
Votey 361 1:00 - 2:15 pm Tue. Th.
Recitation Sessions: Votey 361 3:00 - 3:50 pm M

Instructor
G. Mirchandani 355 Votey Office Hours
mirchand@cems.uvm.edu 10:00 - 11:00 am Tue., Th.

Homeworks Due on Tuesdays by 1:00 p.m., in class.
(Note: Late HWs will not be accepted)

All exams including the final will be closed book.
Prerequisites: EE 4, Math 121

EE171 Software Labs will be held in 229 Votey Hall. If we have time for hardware labs., they will be held in the DSP Lab, 330 Votey Hall.